

DESIGN, TECHNOLOGY AND CHARACTERIZATION OF GALLIUM NITRIDE BASED HIGH POWER HETEROSTRUCTURE FET FOR MICROWAVES¹

Deepak Garg

*Associate Professor, ABES Engineering College, Ghaziabad (India)
Department of Electronics & Communication Engineering*

ABSTRACT

The paper discusses the development of compound elements like AlGa_N/Ga_N, power amplifiers. The paper is on the optimization of the Ti/Al/Ni/Au metallization scheme on a doped AlGa_N/Ga_N FET structure. With systematic investigation the contact resistance was reduced to 0.2 Ω mm (7.3×10^{-7} cm^2) for Al/Ti thickness ratio of 6. For this thickness ratio, as per Al-Ti binary phase diagram, it does not result in excess Ti which should react with nitrogen in the AlGa_N layer to render the surface heavily doped. If prior to metallization, RIE oxygen plasma in combination with a NH₄OH dip is performed, it indicates an improvement in the reverse leakage current.

INTRODUCTION

Towards the development of high-power amplifiers, the utmost importance lies on discrete HEMTs, along with it the matching circuitry is also very important. Amplifiers can be realized either in a hybrid fashion, or in a monolithic fashion (MMIC) with all the elements on the same substrate. The aspects of CPW[6,7] and contacts are important issues in the development of compound elements like AlGa_N/Ga_N, power amplifiers. The following section discusses about the aspects of contacts in the development of compound elements like AlGa_N/Ga_N, power amplifiers.

METAL-SEMICONDUCTOR CONTACTS

Introduction

Till now the researchers present only the final results and not the optimization procedures on low contact resistances and Schottky contacts with low leakage current [1-5]. These two types of contacts are important aspects of AlGa_N/Ga_N FET's influencing gain and thermal behavior. The paper talks on both procedures and results of Ti/Al/Ni/Au.

¹ How to cite the article:

Choudhary D., Kumar R., Gupta N., New Regime for The Synchronization of Timings in Wireless Sensor Network, *International Journal of Advances in Engineering Research*, March 2012, Vol 3, Issue 3, 18-22

Optimization Procedures and Results for Ohmic Contacts

Two identical HEMT wafers consisting of a sapphire substrate, a 40 nm AlN buffer layer, a 2 μm non- intentionally doped (n.i.d.) GaN layer, a 3 nm Al_{0.25}Ga_{0.75}N separation layer, a 10 nm Al_{0.25}Ga_{0.75}N Si-doped donor layer ($1.0 \times 10^{19} \text{ cm}^{-3}$) and a 5 nm n.i.d. Al_{0.25}Ga_{0.75}N contact layer were used. The structure had a sheet resistance of 550 Ω/sq. For cleaning ultrasonic acetone agitation followed by an isopropanol rinse was used. Samples were loaded in the e- beam evaporator without any additional cleaning or acid treatments after the photolithography. The Ti/Al/Ni/Au metallization scheme with different metal thickness ratios and thicknesses were used. Contact parameters were measured using a circular Transmission line model geometry.

By systematically changing the metal composition of the metallization scheme the effect on contact resistance was seen. First the optimal Al/Ti thickness ratio was required to find out. In [1, 2] it was revealed that an excess of Ti can react with the nitrogen of the AlGaN layer rendering the surface highly doped facilitating a tunneling contact. For the Al/Ti thickness ratio of 2.82 for the alloy, the Al/Ti binary phase diagram predicts the formation of Al₃Ti for annealing temperatures above 300 °C [1]. Thus, below 2.82 results in excess Ti, the results are shown in fig.1. The fig.2 shows several different Al/Ti thickness ratios which was tested. The optimal Al/Ti thickness ratio of 6 was used and tried to find the optimal thickness of the Ti layer (figure 2).

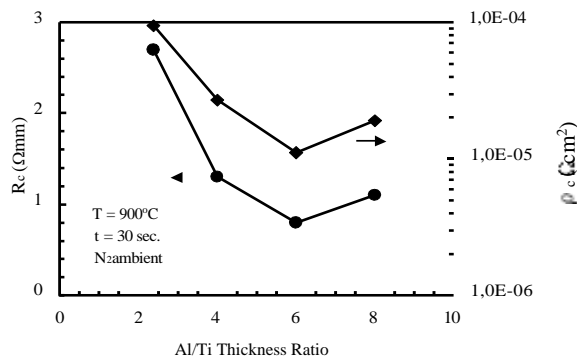


Figure 1: Contact resistance vs. Al/Ti thickness ratio.

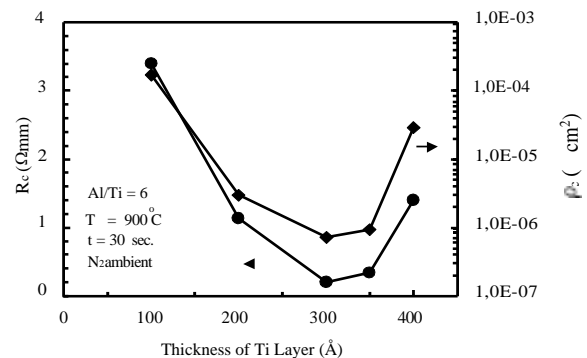


Figure 2: Contact resistance vs. thickness of the Ti layer.

Finally, to optimize the Ni-Au combination was obtained, where the total thickness of the Ni-Au was kept constant at 1900 Å fig. 3. The conclusion was that by selecting the correct Ni thickness is important, although this layer only act as a diffusion barrier. In the above steps RTA step for 30 seconds at 700, 800, 900 and 1000 °C for each sample (sample cleaved into 4 pieces) under N₂ ambient was used. The best annealing conditions for each sample were 30 seconds at 900 °C. All these optimization steps led to the following metallization scheme: Ti (300Å) Al (1800 Å) Ni(400 Å)Au(1500Å).A more detailed optimization of the annealing conditions is illustrated in fig.4, which showed the same result. The optimization detailed above resulted in a contact resistance of 0.2mm (7.3x10⁻⁷ cm²).

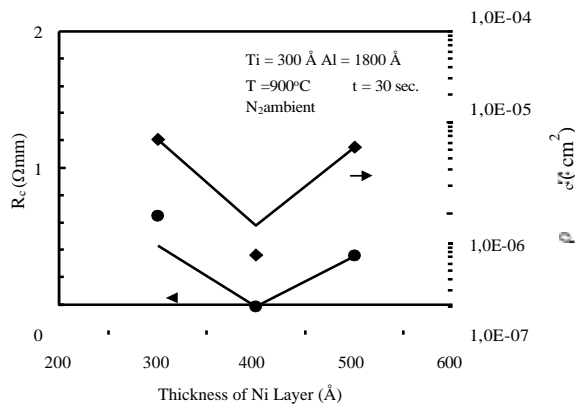


Figure 3: Contact resistance vs. thickness of the Ni layer.

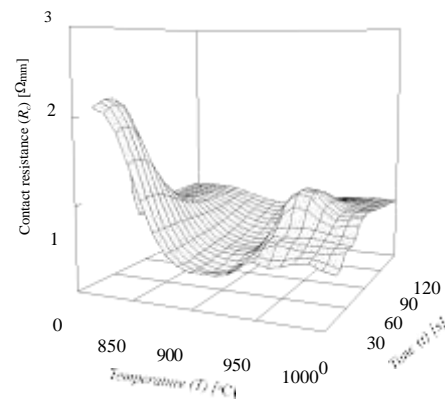


Figure 4: Contact resistance for different RTA temperatures and times under N₂ ambient

Schottky contacts

Circular Schottky diodes (area= $1 \times 10^{-4} \text{ cm}^2$) were made on the same wafers. The initial investigation was focused on optimizing the Ni/Au (200/2000 Å) contact in combination with the optimized Ti/Al/Ni/Au ohmic contact. The following pre-metallization treatments were done: as grown, diluted HCl dip, diluted NH₄OH dip, RIE O₂ plasma, RIE O₂ plasma + diluted NH₄OH dip, RIE O₂ plasma + diluted HCl dip. The O₂ plasma was done at 20 W giving a self-bias of –70 V. The (I-V) data for these experiments are depicted in figure 5. The O₂ plasma followed by a diluted NH₄OH dip showed the best results. The reverse current is relatively constant up to the breakdown voltage, which exceeds 100 V for a spacing of 5 μm. The large reverse current is probably related to the doping used in the structure. (C-V) measurements conducted at 1 MHz showed a non-linear trend in the charge control region fig.6, which predicts a capacitance of 45 pF. This deviation may be related to the small area/edge ratio of the contacts.

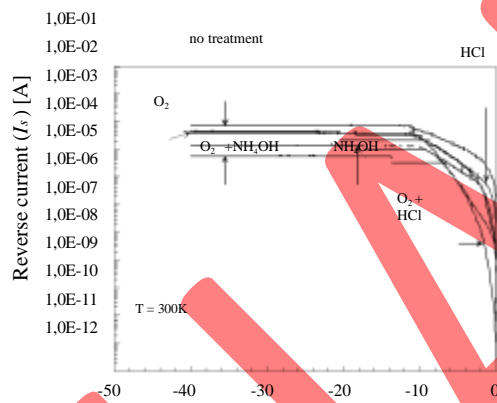


Figure 5: Reverse characteristics of a Ni/Au diode for different pre-treatments.

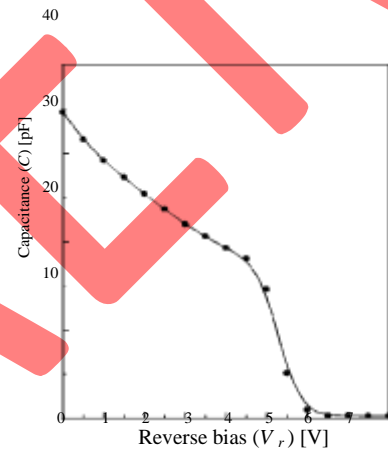


Figure 6: Reverse C-V characteristics for a Ni/Au diode with O₂ plasma + NH₄OH dip.

CONCLUSIONS

This paper presented the optimization procedure for reducing the contact resistance for a Ti/Al/Ni/Au metallization scheme on a doped AlGaIn/GaN FET structure with a sheet resistance of 550 Ω/sq. Ti(300 Å)Al(1800 Å)Ni(400 Å)Au(1500 Å) annealed for 30 seconds at 900 °C in a N₂ ambient resulted in a contact resistance of 0.2 Ω/mm ($7.3 \times 10^{-7} \text{ cm}^2$). The Ni/Au Schottky contacts made on the same material showed high leakage currents probably related to the doping.

REFERENCES

- [1] Ruvimov *et al.*, (1998), *Applied Physics Letters*, Vol.73, no.18, pp.2582-2584.
- [2] Liu *et al.*, (1997); *Applied Physics Letters*, Vol.71, no.12, pp.1658-1660.
- [3] Cai *et al.*, (1998); *Electronic Letters*, Vol.34, pp.2354-2356.
- [4] Qiao *et al.*, (2000), *Journal of Applied Physics*, Vol. 87, pp. 801-804.
- [5] Würfl *et al.*, (1999); *Conference Proceedings GaAs99*, pp. 430-435.
- [6] Williams *et al.*, *IEEE Microwave and Guided Wave Letters*, Vol.1, no. 6, pp. 141-143,191
- [7] Pantoja *et al.*, (1989), *IEEE Microwave Theory and Techniques*, Vol. 37, pp.1675-1680.